

## ABSTRACT OF THE DISCLOSURE

A liquid crystal display device having a first substrate, a second substrate, a liquid crystal layer interposed between the first substrate and the second substrate, a plurality of video signal lines and scanning signal lines formed on the first substrate, and defining pixel regions, a thin film transistor formed in the pixel regions, and driven by a scanning signal from the scanning signal line for supplying video signal from one of the video signal lines to a pixel electrode and a display area containing a plurality of the pixel regions. A first protection element line formed at a peripheral portion of the display area, and being connected to a odd-numbered ones of the video signal lines by first high-resistance elements, and a second protection element line formed at a peripheral portion of the display area, and being connected to even-numbered ones of the video signal lines by second high-resistance elements.